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L1	17	703/27.ccls. and @pd>"20071101"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/04/29 15:20
L4	143	(((tim\$4 or performance) same annotat\$4) same (code or compil\$4)) and simulat\$4 and @ad<"20000601"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/04/29 15:26



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[The SimpleScalar tool set, version 2.0 - all 108 versions »](#)

D Burger, TM Austin - ACM SIGARCH Computer Architecture News, 1997 - portal.acm.org

... that can be modified post-**compile**, with annotations to instructions in the **assembly** files. The **annotation** interface is useful for synthesizing new ...

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[\[PDF\] Evaluating Future Microprocessors: The SimpleScalar Tool Set - all 15 versions »](#)

D Burger, TM Austin, S Bennett - 1996 - csg.lcs.mit.edu

... We designed the instruction set to sup- port easy **annotation** of instructions, without requiring a retar- geted **compiler** for ... Host C **compiler** GCC GAS ... **assembly** ...

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[\[PS\] Incorporating worst-case execution time in a commercial C-compiler - all 7 versions »](#)

H Borjesson - Master's thesis, Department of Computer Systems, Uppsala ..., 1995 - astec.uu.se

... 6 Tests and Results 27 6.1 **Simulation** of the **Compiler** : : :

: 27 ... 30 8.3 \The **Annotation** Approach ...

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[\[PDF\] Integrating WCET Analysis into a Matlab/Simulink **Simulation** Model - all 6 versions »](#)

R Kirner, R Lang, P Puschner, C Temple - Proceedings of the 16th IFAC Workshop on Distributed ..., 2000 - decomsys.com

... and perform- ing WCET analysis at **assembly**-language level ... information automatically by the target-language **compiler** of the ... The **annotated** C code acting as a high ...

Cited by 7 - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[A compilation-based software estimation scheme for hardware/software co-**simulation** - all 11 versions »](#)

M Lajolo, M Lazarescu, A Sangiovanni-Vincentelli - Proceedings of the seventh international workshop on ..., 1999 - portal.acm.org

... This code can be **annotated** with timing information, and ... get binary code, and then **compiling** it on ... because it translates each target **assembly** instruction into ...

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[Trace-driven memory **simulation**: a survey - all 14 versions »](#)

RA Uhlig, TN Mudge - ACM Computing Surveys (CSUR), 1997 - portal.acm.org

... problem is to build small fast memories (caches) to ... Some of the trace-driven **simulation** techniques that we ... Some trace-collection meth- ods **annotate** or emulate ...

Cited by 204 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[DyC: an expressive annotation-directed dynamic compiler for C - all 17 versions »](#)

B Grant, M Mock, M Philipose, C Chambers, SJ ... - Theoretical Computer Science, 2000 - Elsevier

... Integrate, a post-pass that follows **assembly**-code generation ... extensions produced by DyC's **compile**-time phases ... Section 5 then presents the **annotation** lan- guage ...

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[Available Parallelism in Video Applications - all 10 versions »](#)

H Liao, A Wolfe - Proceedings of the International Symposium on ..., 1997 - doi.ieeeecs.org


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K Suzuki, A Sangiovanni-Vincentelli - Annual ACM IEEE Design Automation Conference: Proceedings of ..., 1996
 - doi.ieeecomputersociety.org

... **Efficient Software Performance Estimation Methods for Hardware/Software Codesign**

Kei Suzuki y and Alberto Sangiovanni-Vincentelli z ...

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Hardware-software cosynthesis for digital systems - all 10 versions »

RK Gupta, G De Micheli - Design & Test of Computers, IEEE, 1993 - [ieeexplore.ieee.org](#)

... t' **Specification Performance estimation Trade-offs Constraint analysis detach ... scribe synthesis of hardware or software One way to accomplish this task is ...**

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[book] **Hardware-Software Co-Synthesis of Distributed Embedded Systems** - all 2 versions »

TY Yen, W Wolf - 1996 - Kluwer Academic Publishers Norwell, MA, USA

Cited by 109 - [Related Articles](#) - [Web Search](#)

Performance estimation of embedded software with instruction cache modeling - all 17 versions »

YTS Li, S Malik, A Wolfe - ACM Transactions on Design Automation of Electronic Systems ..., 1999 - [portal.acm.org](#)

... is often required for deciding how **hardware/software** partitioning is ... which models the underlying **hardware** systems and ... speed up the typical **performance** of the ...

Cited by 142 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[book] **Specification and design of embedded systems** - all 3 versions »

DD Gajski, F Vahid, S Narayan, J Gong - 1994 - Prentice-Hall, Inc. Upper Saddle River, NJ, USA

... and trends, Readings in **hardware/software** co-design ... Herkersdorf, Patricia Sagmeister,

Performance evaluation of ... with analytical **estimation**, Computer Networks ...

Cited by 555 - [Related Articles](#) - [Web Search](#)

A compilation-based software estimation scheme for hardware/software co-simulation - all 11 versions »

M Lajolo, M Lazarescu, A Sangiovanni-Vincentelli - ... of the seventh international workshop on **Hardware/software** ..., 1999 - [portal.acm.org](#)

... High-level cost and **performance estimation**, coupled with a fast High-level cost and **performance estimation**, coupled with a fast **hardware/software** co-simulation ...

Cited by 47 - [Related Articles](#) - [Web Search](#)

Real-time Euclid: a language for reliable real-time systems - all 5 versions »

E Kligerman, AD Stoyenko - IEEE Transactions on **Software Engineering**, 1986 - [portal.acm.org](#)

... Kei Suzuki, Alberto Sangiovanni-Vincentelli, **Efficient software performance estimation methods for hardware/software codesign**, Proceedings of the 33rd annual ...

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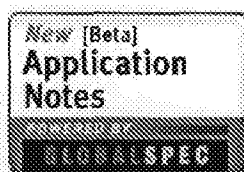
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 Suzuki, K.; Sangiovanni-Vincentelli, A.;
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 3-7 June 1996 Page(s):605 - 610
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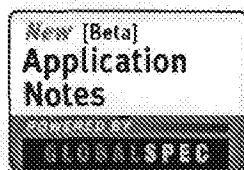

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